National Semiconductor

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LM7301
Low Power, 4 MHz GBW, Rail-to-Rail Input-Output Operational Amplifier in TinyPak™ Package

General Description

The LM7301 provides high performance in a wide range of applications. The LM7301 offers greater than rail-to-rail input range, full rail-to-rail output swing, large capacitive load driving ability and low distortion.

With only 0.6 mA supply current, the 4 MHz gain-bandwidth of this device supports new portable applications where higher power devices unacceptably drain battery life.

The LM7301 can be driven by voltages that exceed both power supply rails, thus eliminating concerns over exceeding the common-mode voltage range. The rail-to-rail output swing capability provides the maximum possible dynamic range at the output. This is particularly important when operating on low supply voltages.

Operating on supplies of 1.8V–32V, the LM7301 is excellent for a very wide range of applications in low power systems.

Placing the amplifier right at the signal source reduces board size and simplifies signal routing. The LM7301 fits easily on low profile PCMCIA cards.

Features

at V_S = 5V (Typ unless otherwise noted)

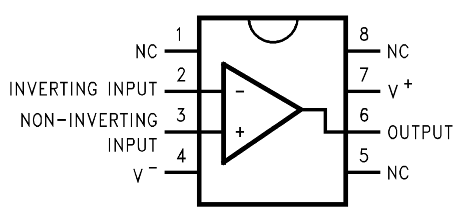
- Tiny 5-pin SOT23 package saves space
- Greater than Rail-to-Rail Input CMVR −0.25V to 5.25V
- Rail-to-Rail Output Swing 0.07V to 4.93V
- Wide Gain-Bandwidth 4 MHz
- Low Supply Current 0.60 mA
- Wide Supply Range 1.8V to 32V
- High PSRR 104 dB
- High CMRR 93 dB
- Excellent Gain 97 dB

Applications

- Portable instrumentation
- Signal conditioning amplifiers/ADC buffers
- Active filters
- Modems
- PCMCIA cards

Connection Diagrams

8-Pin SOIC



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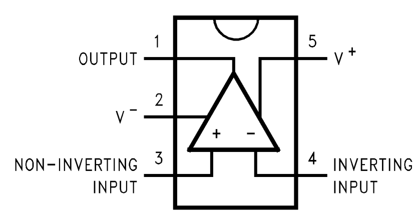
Top View

5-Pin SOT23



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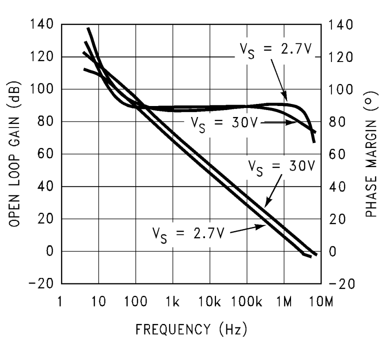
Top View

Gain and Phase



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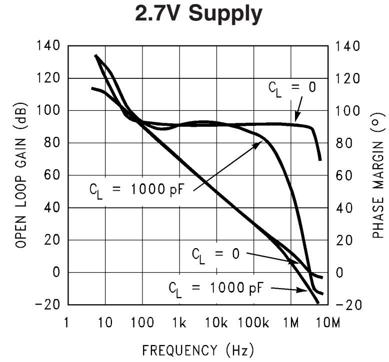
Gain and Phase, 2.7V Supply



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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

ESD Tolerance (Note 2)	2500V
Differential Input Voltage	15V
Voltage at Input/Output Pin	$(V^+) + 0.3V$, $(V^-) -0.3V$
Supply Voltage ($V^+ - V^-$)	35V
Current at Input Pin	±10 mA
Current at Output Pin (Note 3)	±20 mA
Current at Power Supply Pin	25 mA
Lead Temperature	
(Soldering, 10 sec.)	260˚C
Storage Temperature Range	−65˚C to +150˚C
Junction Temperature (Note 4)	150˚C

Operating Ratings (Note 1)

Supply Voltage	$1.8V \le V_S \le 32V$
Junction Temperature Range	$-40˚C \le T_J \le +85˚C$
Thermal Resistance (θ_{JA})	
5-Pin SOT23	325˚C/W
8-Pin SOIC	165˚C/W

5.0V DC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_J = 25˚C, V^+ = 5.0V, V^- = 0V, $V_{CM} = V_O = V^+/2$ and R_L > 1 MΩ to $V^+/2$. **Boldface** limits apply at the temperature extremes

Symbol	Parameter	Conditions	Typ (Note 5)	LM7301 Limit (Note 6)	Units
V_{OS}	Input Offset Voltage		0.03	6 **8**	mV max
TCV_{OS}	Input Offset Voltage Average Drift		**2**		μV/˚C
I_B	Input Bias Current	V_{CM} = 0V	90	200 **250**	nA max
		V_{CM} = 5V	−40	−75 **−85**	nA min
I_{OS}	Input Offset Current	V_{CM} = 0V	0.7	70 **80**	nA max
		V_{CM} = 5V	0.7	55 **65**	
R_{IN}	Input Resistance, CM	$0V \le V_{CM} \le 5V$	39		MΩ
CMRR	Common Mode Rejection Ratio	$0V \le V_{CM} \le 5V$	88	70 **67**	dB min
		$0V \le V_{CM} \le 3.5V$	93		
PSRR	Power Supply Rejection Ratio	$2.2V \le V^+ \le 30V$	104	87 **84**	
V_{CM}	Input Common-Mode Voltage Range	CMRR ≥ 65 dB	5.1		V
			−0.1		V
A_V	Large Signal Voltage Gain	R_L = 10 kΩ $V_O = 4.0V_{PP}$	71	14 **10**	V/mV min
V_O	Output Swing	R_L = 10 kΩ	0.07	0.12 **0.15**	V max
			4.93	4.88 **4.85**	V min
		R_L = 2 kΩ	0.14	0.20 **0.22**	V max
			4.87	4.80 **4.78**	V min
I_{SC}	Output Short Circuit Current	Sourcing	11.0	8.0 **5.5**	mA min
		Sinking	9.5	6.0 **5.0**	mA min

5.0V DC Electrical Characteristics (Continued)

Unless otherwise specified, all limits guaranteed for $T_J = 25^\circ C$, $V^+ = 5.0V$, $V^- = 0V$, $V_{CM} = V_O = V^+/2$ and $R_L > 1\ M\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes

Symbol	Parameter	Conditions	Typ (Note 5)	LM7301 Limit (Note 6)	Units
I_S	Supply Current		0.60	1.10 **1.24**	mA max

AC Electrical Characteristics

$T_J = 25^\circ C$, $V^+ = 2.2V$ to $30V$, $V^- = 0V$, $V_{CM} = V_O = V^+/2$ and $R_L > 1\ M\Omega$ to $V^+/2$

Symbol	Parameter	Conditions	Typ (Note 5)	Units
SR	Slew Rate	±4V Step @ V_S ±6V	1.25	V/µs
GBW	Gain-Bandwidth Product	f = 100 kHz, $R_L = 10\ k\Omega$	4	MHz
e_n	Input-Referred Voltage Noise	f = 1 kHz	36	$\frac{nV}{\sqrt{Hz}}$
i_n	Input-Referred Current Noise	f = 1 kHz	0.24	$\frac{pA}{\sqrt{Hz}}$
T.H.D.	Total Harmonic Distortion	f = 10 kHz	0.006	%

2.2V DC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for $T_J = 25^\circ C$, $V^+ = 2.2V$, $V^- = 0V$, $V_{CM} = V_O = V^+/2$ and $R_L > 1\ M\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes

Symbol	Parameter	Conditions	Typ (Note 5)	LM7301 Limit (Note 6)	Units
V_{OS}	Input Offset Voltage		0.04	6 **8**	mV max
TCV_{OS}	Input Offset Voltage Average Drift		**2**		µV/°C
I_B	Input Bias Current	$V_{CM} = 0V$	89	200 **250**	nA max
		$V_{CM} = 2.2V$	−35	−75 **−85**	nA min
I_{OS}	Input Offset Current	$V_{CM} = 0V$	0.8	70 **80**	nA max
		$V_{CM} = 2.2V$	0.4	55 **65**	
R_{IN}	Input Resistance	$0V \le V_{CM} \le 2.2V$	18		MΩ
CMRR	Common Mode Rejection Ratio	$0V \le V_{CM} \le 2.2V$	82	60 **56**	dB min
PSRR	Power Supply Rejection Ratio	$2.2V \le V^+ \le 30V$	104	87 **84**	
V_{CM}	Input Common-Mode Voltage Range	CMRR > 60 dB	2.3		V
			−0.1		V
A_V	Large Signal Voltage Gain	$R_L = 10\ k\Omega$ $V_O = 1.6V_{PP}$	46	6.5 **5.4**	V/mV min

2.2V DC Electrical Characteristics (Continued)

Unless otherwise specified, all limits guaranteed for T_J = 25˚C, V^+ = 2.2V, V^- = 0V, V_{CM} = V_O = V^+/2 and R_L > 1 MΩ to V^+/2. **Boldface** limits apply at the temperature extremes

Symbol	Parameter	Conditions	Typ (Note 5)	LM7301 Limit (Note 6)	Units
V_O	Output Swing	R_L = 10 kΩ	0.05	0.08 **0.10**	V max
			2.15	2.10 **2.00**	V min
		R_L = 2 kΩ	0.09	0.13 **0.14**	V max
			2.10	2.07 **2.00**	V min
I_{SC}	Output Short Circuit Current	Sourcing	10.9	8.0 **5.5**	mA min
		Sinking	7.7	6.0 **5.0**	mA min
I_S	Supply Current		0.57	0.97 **1.24**	mA max

30V DC Electrical Characteristics

Unless otherwise specified, all limits guaranteed for T_J = 25˚C, V^+ = 30V, V^- = 0V, V_{CM} = V_O = V^+/2 and R_L > 1 MΩ to V^+/2. **Boldface** limits apply at the temperature extremes

Symbol	Parameter	Conditions	Typ (Note 5)	LM7301 Limit (Note 6)	Units
V_{OS}	Input Offset Voltage		0.04	6 **8**	mV max
TCV_{OS}	Input Offset Voltage Average Drift		**2**		μV/˚C
I_B	Input Bias Current	V_{CM} = 0V	103	300 **500**	nA max
		V_{CM} = 30V	–50	–100 **–200**	nA min
I_{OS}	Input Offset Current	V_{CM} = 0V	1.2	90 **190**	nA max
		V_{CM} = 30V	0.5	65 **135**	nA max
R_{IN}	Input Resistance	0V ≤ V_{CM} ≤ 30V	200		MΩ
CMRR	Common Mode Rejection Ratio	0V ≤ V_{CM} ≤ 30V	104	80 **78**	dB min
		0V ≤ V_{CM} ≤ 27V	115	90 **88**	
PSRR	Power Supply Rejection Ratio	2.2V ≤ V^+ ≤ 30V	104	87 **84**	
V_{CM}	Input Common-Mode Voltage Range	CMRR > 80 dB	30.1		V
			–0.1		V
A_V	Large Signal Voltage Gain	R_L = 10 kΩ, V_O = $28V_{PP}$	105	30 **20**	V/mV min

30V DC Electrical Characteristics (Continued)

Unless otherwise specified, all limits guaranteed for T_J = 25˚C, V^+ = 30V, V^- = 0V, V_{CM} = V_O = V^+/2 and R_L > 1 MΩ to V^+/2. **Boldface** limits apply at the temperature extremes

Symbol	Parameter	Conditions	Typ (Note 5)	LM7301 Limit (Note 6)	Units
V_O	Output Swing	R_L = 10 kΩ	0.16	0.275 **0.375**	V max
			29.8	29.75 **28.65**	V min
I_{SC}	Output Short Circuit Current	Sourcing (Note 4)	11.7	8.8 **6.5**	mA min
		Sinking (Note 4)	11.5	8.2 **6.0**	mA min
I_S	Supply Current		0.72	1.30 **1.35**	mA max

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.

Note 2: Human Body Model is 1.5 kΩ in series with 100 pF.

Note 3: Applies to both single-supply and split-supply operation. Continuous short circuit operation at elevated ambient temperature can result in exceeding the maximum allowed junction temperature of 150˚C.

Note 4: The maximum power dissipation is a function of $T_{J(MAX)}$, θ_{JA}, and T_A. The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A)/\theta_{JA}$. All numbers apply for packages soldered directly into a PC board.

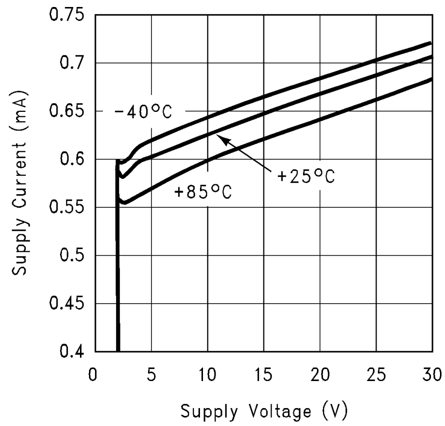
Note 5: Typical Values represent the most likely parametric norm.

Note 6: All limits are guaranteed by testing or statistical analysis.

Typical Performance Characteristics

T_A = 25˚C, R_L = 1 MΩ unless otherwise specified

Supply Current vs. Supply Voltage



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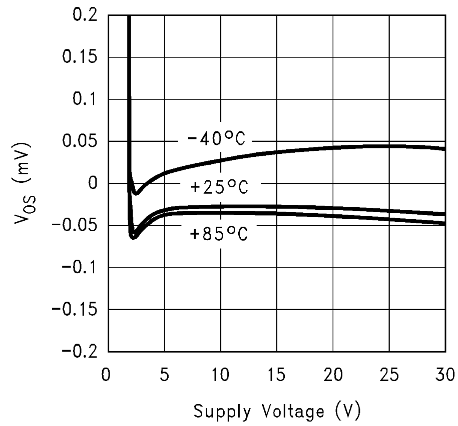
V_{OS} vs. Supply Voltage



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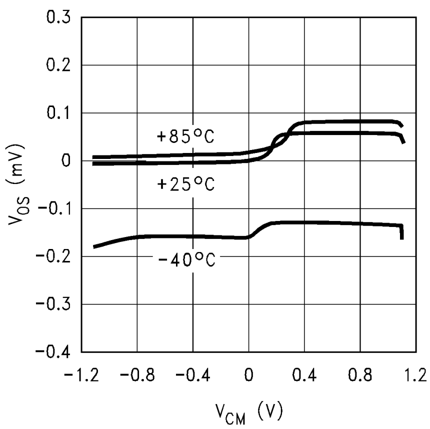
V_{OS} vs. V_{CM}
V_S = ± 1.1V



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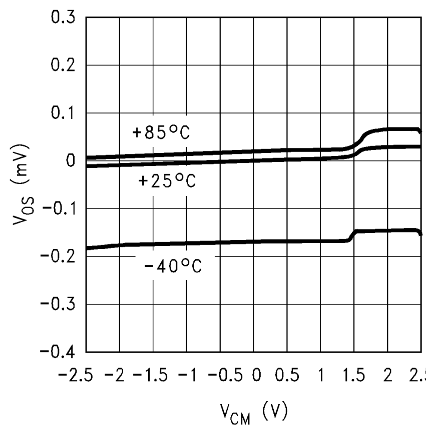
V_{OS} vs. V_{CM}
V_S = ± 2.5V



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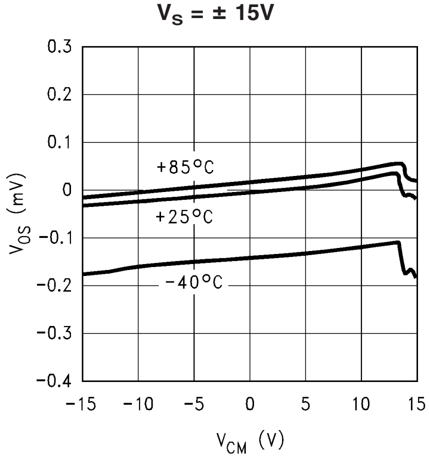
V_{OS} vs. V_{CM}
V_S = ± 15V



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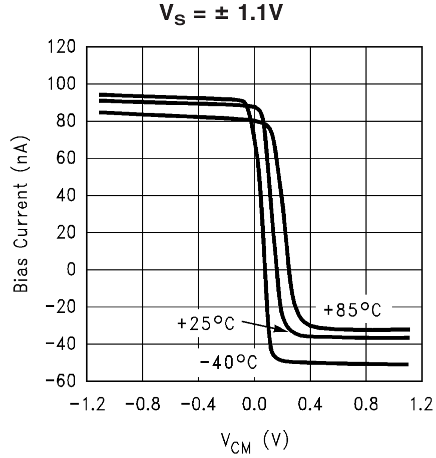
Inverting Input Bias Current vs. Common Mode Voltage
V_S = ± 1.1V



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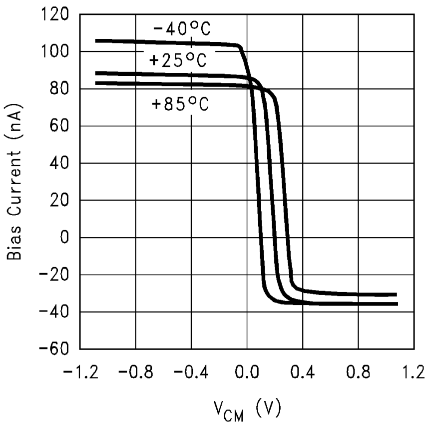
Typical Performance Characteristics $T_A = 25^\circ C$, $R_L = 1\ M\Omega$ unless otherwise specified (Continued)

Non-Inverting Input Bias Current vs. Common Mode Voltage
$V_S = \pm 1.1V$



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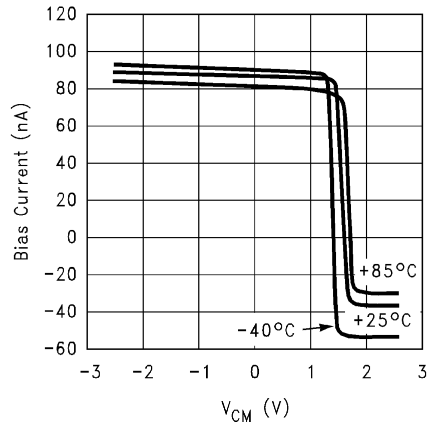
Inverting Input Bias Current vs. Common Mode Voltage
$V_S = \pm 2.5V$



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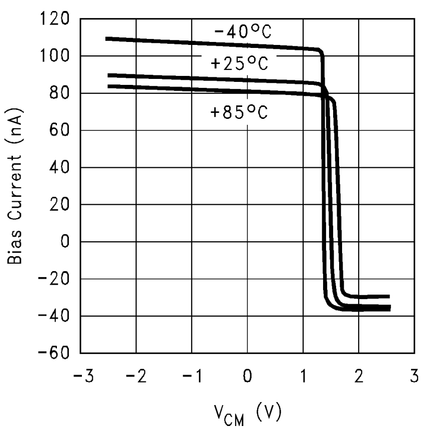
Non-Inverting Input Bias Current vs. Common Mode Voltage
$V_S = \pm 2.5V$



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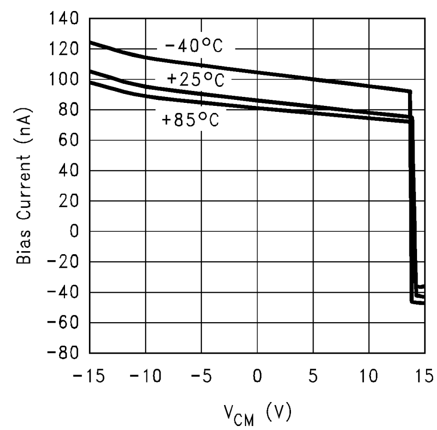
Non-Inverting Input Bias Current vs. Common Mode Voltage
$V_S = \pm 15V$



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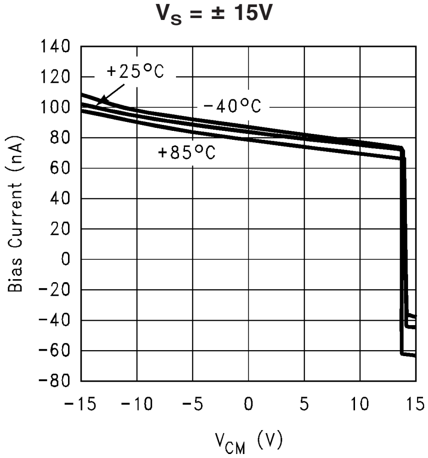
Inverting Input Bias Current vs. Common Mode Voltage
$V_S = \pm 15V$



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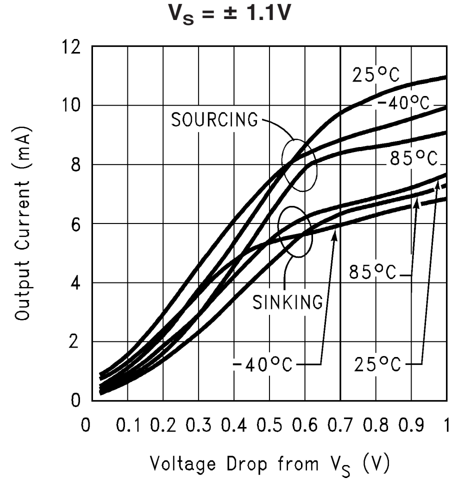
V_O vs. I_O
$V_S = \pm 1.1V$



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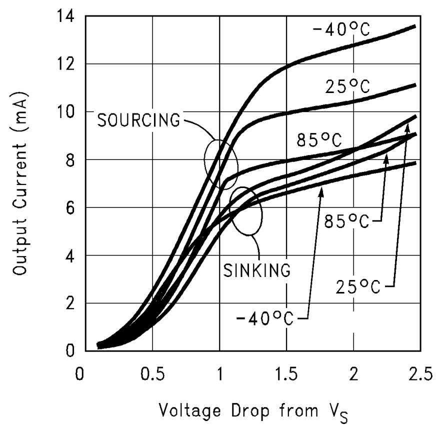
Typical Performance Characteristics T_A = 25˚C, R_L = 1 MΩ unless otherwise specified (Continued)

V_O vs. I_O
V_S = ± 2.5V



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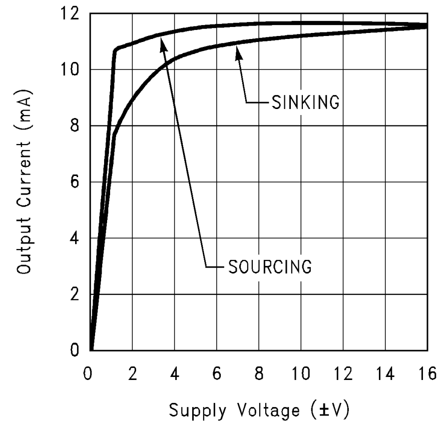
Short Circuit Current vs. Supply Voltage



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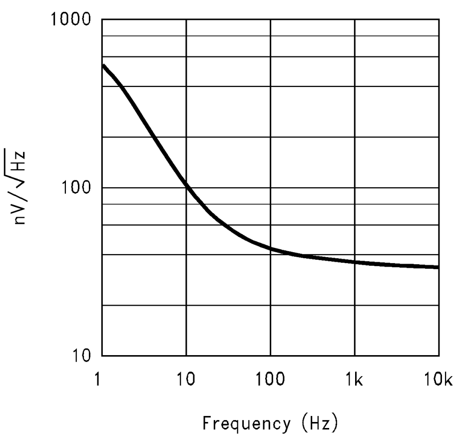
Voltage Noise vs. Frequency



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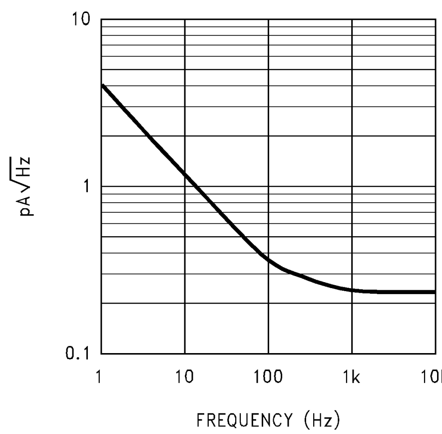
Current Noise vs. Frequency



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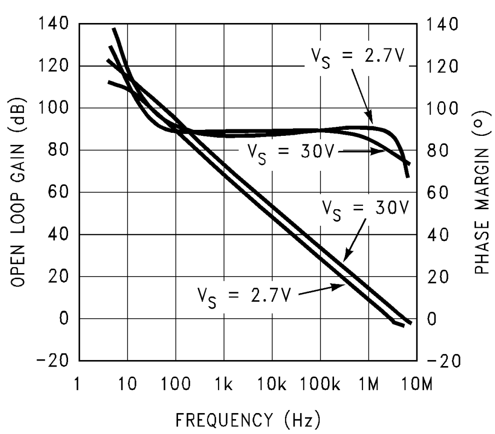
Gain and Phase



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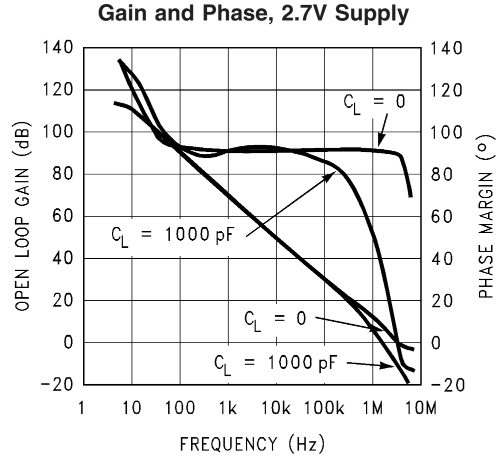
Gain and Phase, 2.7V Supply



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Applications Information

GENERAL INFORMATION

Low supply current and wide bandwidth, greater than rail-to-rail input range, full rail-to-rail output, good capacitive load driving ability, wide supply voltage and low distortion all make the LM7301 ideal for many diverse applications.

The high common-mode rejection ratio and full rail-to-rail input range provides precision performance when operated in non-inverting applications where the common-mode error is added directly to the other system errors.

CAPACITIVE LOAD DRIVING

The LM7301 has the ability to drive large capacitive loads. For example, 1000 pF only reduces the phase margin to about 25 degrees.

TRANSIENT RESPONSE

The LM7301 offers a very clean, well-behaved transient response. *Figures 1, 2, 3, 4, 5, 6* show the response when operated at gains of +1 and −1 when handling both small and large signals. The large phase margin, typically 70 to 80 degrees, assures clean and symmetrical response. In the large signal scope photos, *Figure 1* and *Figure 4*, the input signal is set to 4.8V. Note that the output goes to within 100 mV of the supplies cleanly and without overshoot. In the small signal samples, the response is clean, with only slight overshoot when used as a follower. *Figure 3* and *Figure 6* are the circuits used to make these photos.

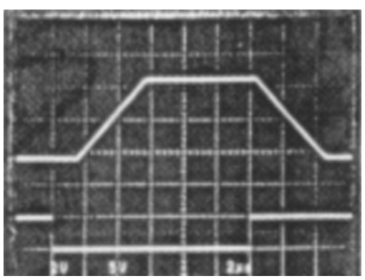

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FIGURE 1.

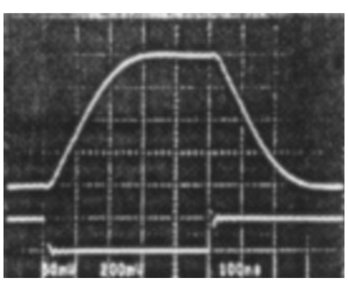

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FIGURE 2.

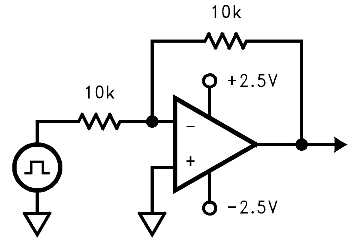

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FIGURE 3.

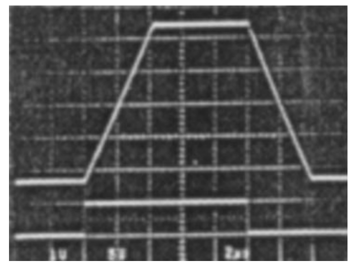

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FIGURE 4.

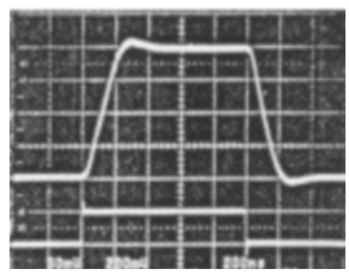

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FIGURE 5.

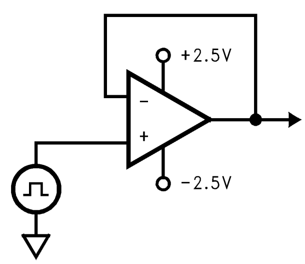

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FIGURE 6.

POWER DISSIPATION

Although the LM7301 has internal output current limiting, shorting the output to ground when operating on a +30V power supply will cause the op amp to dissipate about 350 mW. This is a worst-case example. In the 8-pin SOIC package, this will cause a temperature rise of 58˚C. In the 5-pin SOT23 package, the higher thermal resistance will

Applications Information (Continued)

cause a calculated rise of 113˚C. This can raise the junction temperature to above the absolute maximum temperature of 150˚C.

Operating from split supplies greatly reduces the power dissipated when the output is shorted. Operating on ±15V supplies can only cause a temperature rise of 29˚C in the 8-pin SOIC and 57˚C in the 5-pin SOT23 package, assuming the short is to ground.

SPICE Macromodel

A SPICE macromodel for this and many other National Semiconductor operational amplifiers is available, at no charge, from the NSC Customer Support Center at 800-272-9959 or on the World Wide Web at http://www.national.com/models.

WIDE SUPPLY RANGE

The high power-supply rejection ratio (PSRR) and common-mode rejection ratio (CMRR) provide precision performance when operated on battery or other unregulated supplies. This advantage is further enhanced by the very wide supply range (2.2V–30V, guaranteed) offered by the LM7301. In situations where highly variable or unregulated supplies are present, the excellent PSRR and wide supply range of the LM7301 benefit the system designer with continued precision performance, even in such adverse supply conditions.

SPECIFIC ADVANTAGES OF 5-Pin SOT23 (TinyPak)

The obvious advantage of the 5-pin SOT23, TinyPak, is that it can save board space, a critical aspect of any portable or miniaturized system design. The need to decrease overall system size is inherent in any handheld, portable, or light-weight system application.

Furthermore, the low profile can help in height limited designs, such as consumer hand-held remote controls, sub-notebook computers, and PCMCIA cards.

An additional advantage of the tiny package is that it allows better system performance due to ease of package placement. Because the tiny package is so small, it can fit on the board right where the op amp needs to be placed for optimal performance, unconstrained by the usual space limitations. This optimal placement of the tiny package allows for many system enhancements, not easily achieved with the constraints of a larger package. For example, problems such as system noise due to undesired pickup of digital signals can be easily reduced or mitigated. This pick-up problem is often caused by long wires in the board layout going to or from an op amp. By placing the tiny package closer to the signal source and allowing the LM7301 output to drive the long wire, the signal becomes less sensitive to such pick-up. An overall reduction of system noise results.

Often times system designers try to save space by using dual or quad op amps in their board layouts. This causes a complicated board layout due to the requirement of routing several signals to and from the same place on the board. Using the tiny op amp eliminates this problem.

Additional space savings parts are available in tiny packages from National Semiconductor, including low power amplifiers, precision voltage references, and voltage regulators.

LOW DISTORTION, HIGH OUTPUT DRIVE CAPABILITY

The LM7301 offers superior low-distortion performance, with a total-harmonic-distortion-plus-noise of 0.06% at f = 10 kHz. The advantage offered by the LM7301 is its low distortion levels, even at high output current and low load resistance.

Typical Applications

HANDHELD REMOTE CONTROLS

The LM7301 offers outstanding specifications for applications requiring good speed/power trade-off. In applications such as remote control operation, where high bandwidth and low power consumption are needed. The LM7301 performance can easily meet these requirements.

OPTICAL LINE ISOLATION FOR MODEMS

The combination of the low distortion and good load driving capabilities of the LM7301 make it an excellent choice for driving opto-coupler circuits to achieve line isolation for modems. This technique prevents telephone line noise from coupling onto the modem signal. Superior isolation is achieved by coupling the signal optically from the computer modem to the telephone lines; however, this also requires a low distortion at relatively high currents. Due to its low distortion at high output drive currents, the LM7301 fulfills this need, in this and in other telecom applications.

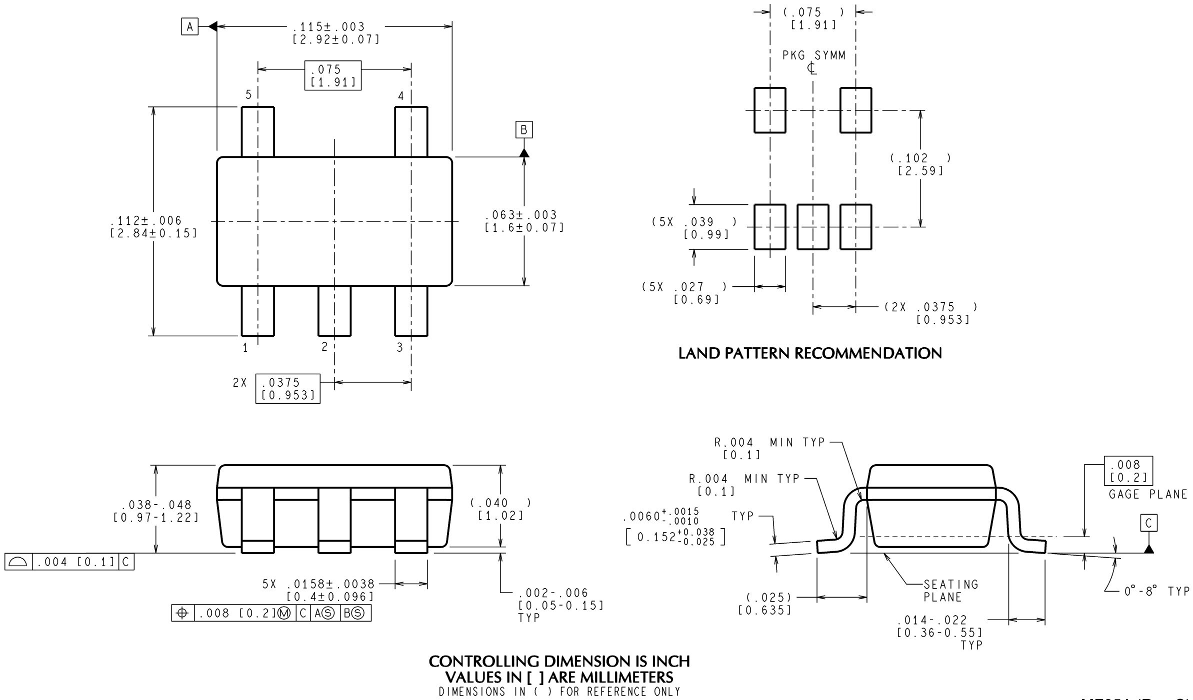
REMOTE MICROPHONE IN PERSONAL COMPUTERS

Remote microphones in Personal Computers often utilize a microphone at the top of the monitor which must drive a long cable in a high noise environment. One method often used to reduce the nose is to lower the signal impedance, which reduces the noise pickup. In this configuration, the amplifier usually requires 30 db–40 db of gain, at bandwidths higher than most low-power CMOS parts can achieve. The LM7301 offers the tiny package, higher bandwidths, and greater output drive capability than other rail-to-rail input/output parts can provide for this application.

Ordering Information

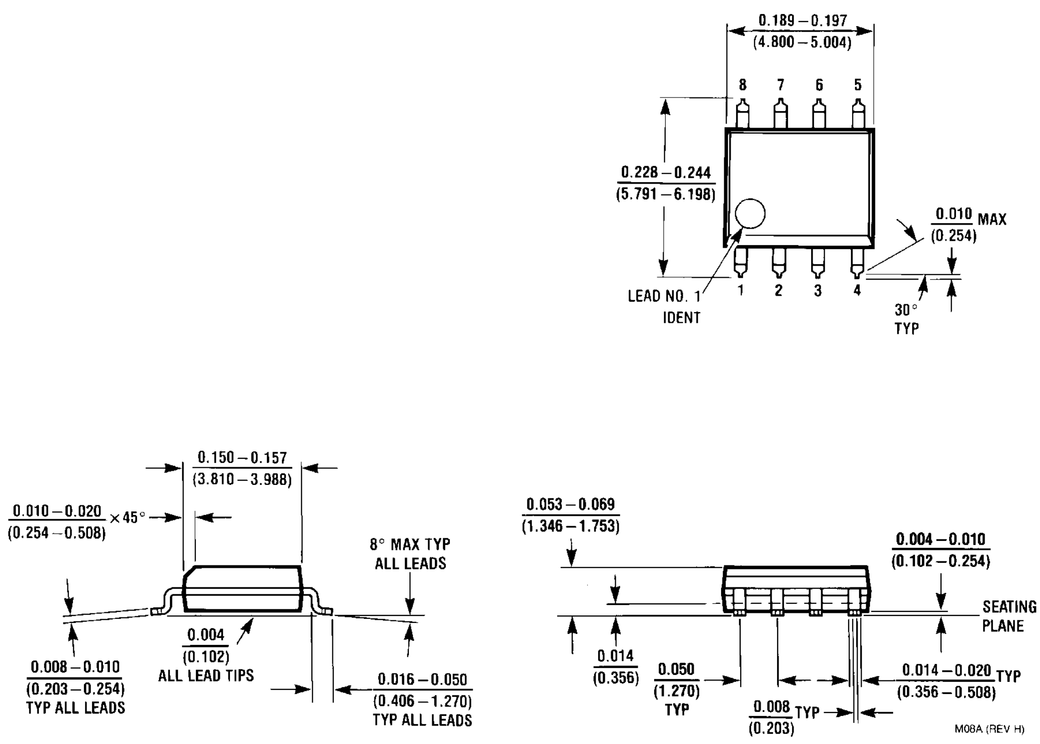
Package	Part Number	Package Marking	Transport Media	NSC Drawing
8-Pin SOIC	LM7301IM	LM7301IM	95 Units/Rail	M08A
	LM7301IMX		2.5k Units Tape and Reel	
5-Pin SOT23	LM7301IM5	A04A	1k Units Tape and Reel	MF05A
	LM7301IM5X		3k Units Tape and Reel	

Physical Dimensions inches (millimeters) unless otherwise noted



MF05A (Rev C)

5-Pin SOT23 Package
NS Package Number MF05A



8-Pin SOIC
NS Package Number M08A

Notes

National Semiconductor
Americas Customer
Support Center
Email: new.feedback@nsc.com
Tel: 1-800-272-9959

National Semiconductor
Europe Customer Support Center
Fax: +49 (0) 180-530 85 86
Email: europe.support@nsc.com
Deutsch Tel: +49 (0) 69 9508 6208
English Tel: +44 (0) 870 24 0 2171
Français Tel: +33 (0) 1 41 91 8790

National Semiconductor
Asia Pacific Customer
Support Center
Email: ap.support@nsc.com

National Semiconductor
Japan Customer Support Center
Fax: 81-3-5639-7507
Email: jpn.feedback@nsc.com
Tel: 81-3-5639-7560